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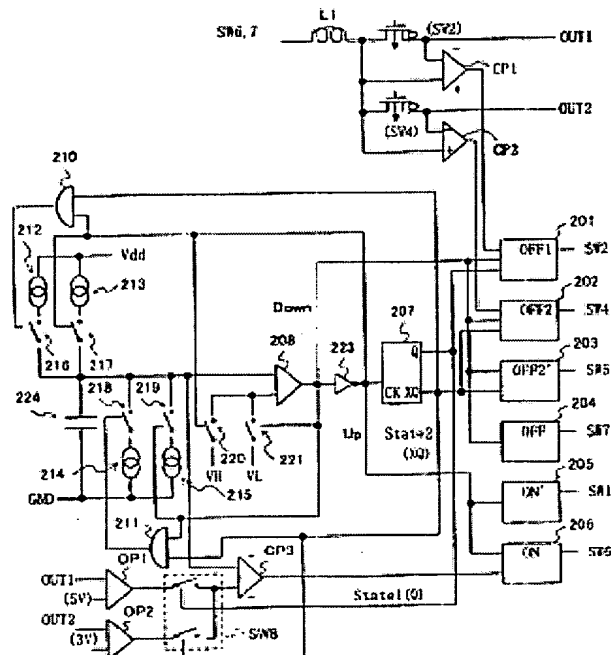
### REMARKS

By this amendment, claims 1, 5, 9, and 13 have been amended. Claims 1-19 are pending in the application. Applicants reserve the right to pursue the original claims and other claims in this and other applications.

Claims 1-3, 5-7, 9-11, 13-15, and 19 stand rejected under 35 U.S.C. § 102(e) as being anticipated by Nishimori et al. (US 6,900,620). This rejection is respectfully traversed.

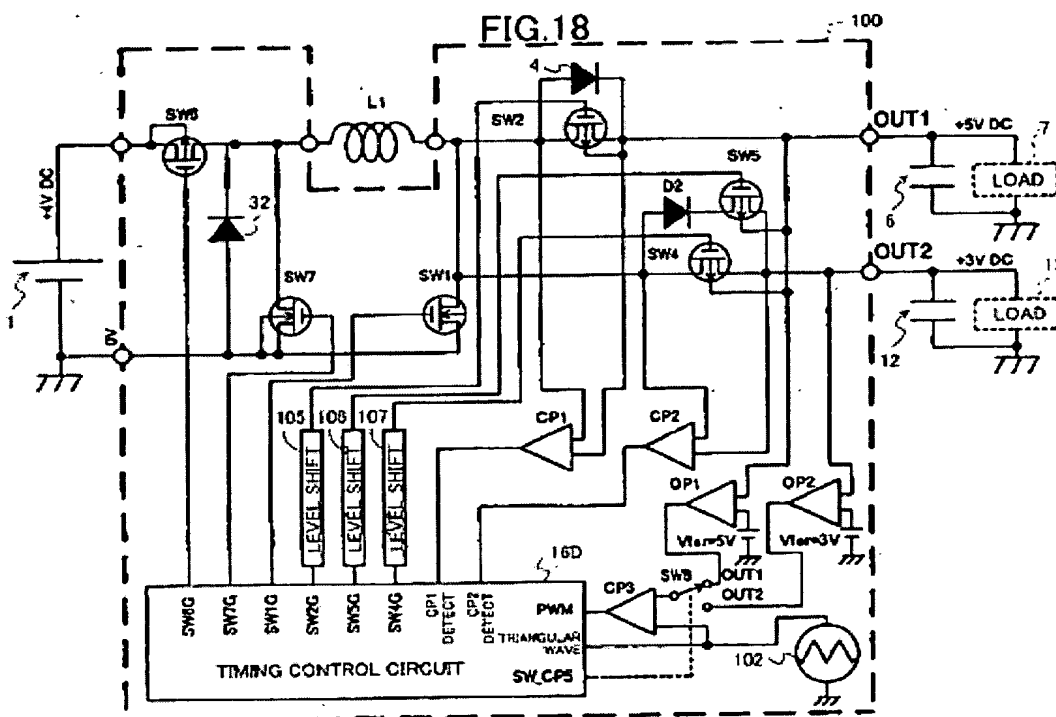
Claims 1, 5, 9, and 13, as amended, recite a power supply apparatus or method comprising, *inter alia*, a controller or controlling means “comprising a plurality of control circuits respectively coupled to control said plurality of power source circuits, said plurality of control circuits commonly receiving said common oscillator signal performing a time sharing control based on the oscillator signal to enable the plurality of power source circuits to receive power from the common direct current power source in different timings and to output respective voltages” (emphasis added). Nishimori et al. does not disclose this limitation. Nishimori et al. discloses only a single control circuit 16D, which comprises assorted circuitry, none of which is a plurality of control circuits that commonly receiving said common oscillator signal, as recited in claims 1, 5, 9, and 13. Nishimori et al. FIG. 20 (reproduced below). Since Nishimori et al. does not disclose all the limitations of claims 1, 5, 9, and 13, claims 1, 5, 9, and 13 are not anticipated by Nishimori et al.

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Nishimori et al. FIG. 20

Claim 19 recites a power supply apparatus comprising, *inter alia*, "a plurality of power source circuits with a common direct current power source, at least one of the plurality of power source circuits comprising: an electric storage circuit; a semiconductor switch ...; an error amplifier ...; and a controlling circuit." Nishimori et al. does not disclose this limitation. Nishimori et al. discloses switches SW2 and SW4 connected to outputs OUT1 and OUT2. Nishimori et al. FIG. 18 (reproduced below). There is no electric storage circuit, semiconductor switch, error amplifier, or controlling circuit in the power supply circuits. Since Nishimori et al. does not disclose all the limitations of claim 19, claim 19 is not anticipated by Nishimori et al.

Nishimori et al. FIG. 18



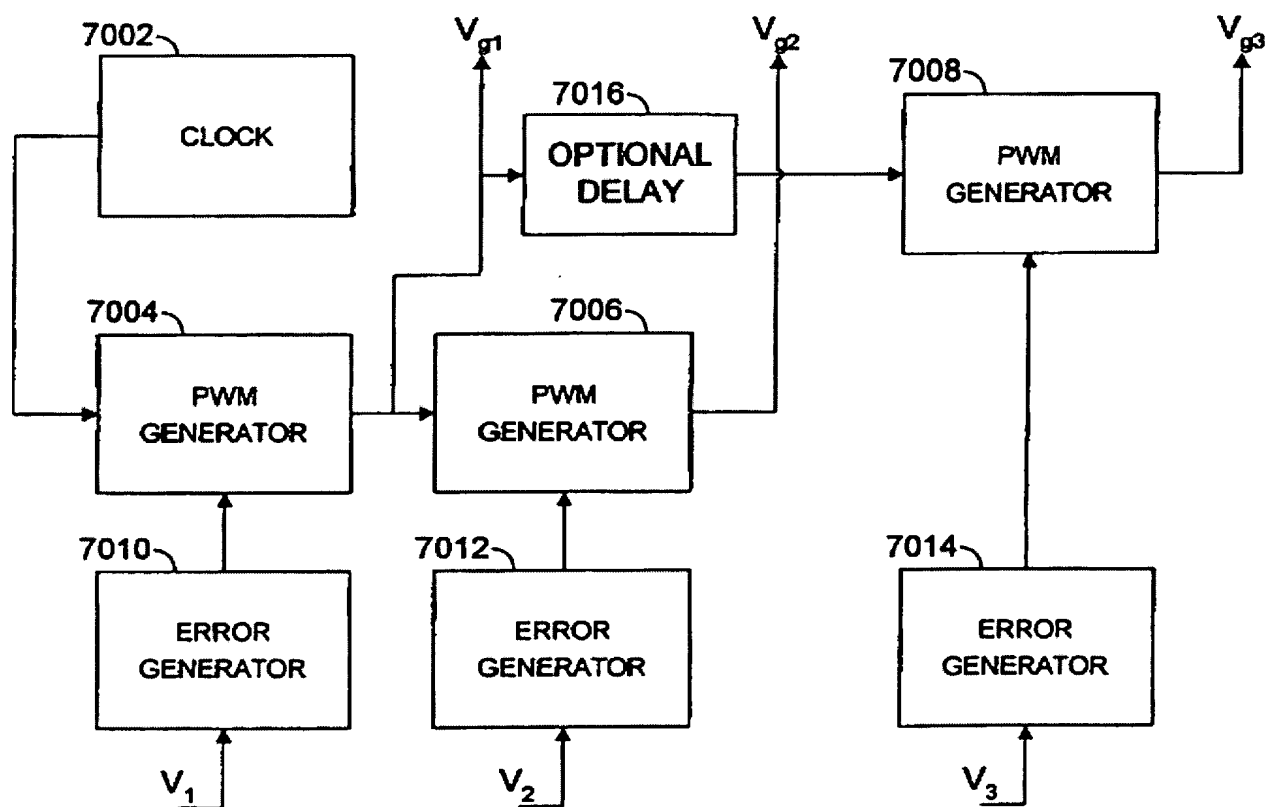
Claims 1-3, 5-7, 9-11, 13-15, and 19 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Aas et al. (US 6,265,855). This rejection is respectfully traversed.

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said plurality of control circuits commonly receiving said common oscillator signal performing a time sharing control based on the oscillator signal to enable the plurality of power source circuits to receive power from the common direct current power source in different timings and to output respective voltages" (emphasis added). Aas et al. does not disclose this limitation. Aas et al. discloses in FIG. 7 that PWM generators 7006 and 7008 operate based on the output from PWM generator 7004, and not on clock signal 7002. Aas et al. FIG 7 (reproduced below). There is no controller comprising a plurality of control circuits commonly receiving said common oscillator signal as recited in claims 1, 5, 9, and 13.

Aas et al. FIG. 7



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Since Aas et al. does not disclose all the limitations of claims 1, 5, 9, and 13, claims 1, 5, 9, and 13 are not anticipated by Aas et al. Claims 2-3 depend from claim 1 and are patentable at least for the reasons mentioned above. Claims 6-7 depend from claim 6 and are patentable at least for the reasons mentioned above. Claims 10-11 depend from claim 9 and are patentable at least for the reasons mentioned above. Claims 14-15 depend from claim 13 and are patentable at least for the reasons mentioned above. Applicants respectfully request that the 35 U.S.C. § 102(b) rejection of claims 1-3, 5-7, 9-11, 13-15, and 19 be withdrawn.

Claim 19 recites a power supply apparatus comprising, *inter alia*, "a plurality of power source circuits with a common direct current power source, at least one of the plurality of power source circuits comprising: an electric storage circuit; a semiconductor switch ...; an error amplifier ...; and a controlling circuit." Aas et al. does not disclose this limitation. Aas et al. discloses "first regulator 1010 is shown comprising switching transistor S1, 1012, diode 1014, inductor L1, 105, and filter capacitor C1 1016." Col. 2, ln. 63-65. There is no electric storage circuit, semiconductor switch, error amplifier, or controlling circuit in the power supply circuits. Since Aas et al. does not disclose all the limitations of claim 19, claim 19 is not anticipated by Aas et al.

Claims 4, 8, 12, and 16-18 stand objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claims and any intervening claims. Claims 4, 8, 12, and 16-18 depend, respectively, directly, or indirectly from independent claims 1, 5, 9, and 13, and are allowable for at least the reasons set forth above.

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In view of the above amendment, Applicants believe the pending application is in condition for allowance.

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Respectfully submitted,

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